



**National Institute of Electronics and Information Technology  
NIELIT Bhawan, Plot No. 3, PSP Pocket, Sector-8,  
Dwarka, New Delhi-110077**

**WALK-IN-INTERVIEW [DATE- 13.07.2026, 14.07.2026 and 15.07.2026]**

NIELIT Invites Applications from suitable, interested and eligible candidates for the following position(s): - **[1]** Team Lead (VLSI Design) **[2]** VLSI Design Expert/ Junior VLSI Engineer **[3]** Senior Trainers (VLSI Design)/ Senior VLSI Engineer **[4]** Junior DevOps Engineer **[5]** Junior Full Stack Engineer **[6]** Project Assistant-VLSI purely on temporary and short-term contract basis on consolidated remuneration initially for a period of one year which may be considered for extension based on performance and requirement.

Interested candidates may bring completely filled Application Form alongwith photograph and self-attested Certificate in support of qualification & experience as per the walk-in-interview scheduled uploaded on Nielit website. Candidate has to be deposit the non-refundable registration fee of Rs. 500/- per position through online mode on or before walk-in-interview date. For detailed advt. with eligibility criteria, remuneration, age, terms and conditions and application pro-forma log on to <https://nielit.gov.in/recruitments>

**Advt. No. NHQ-35/24/2026-NIELIT [3175427]**

**Registrar**