

**NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION  
TECHNOLOGY, CoE NOIDA CENTRE**

Date: 18-03-2026

Result of Walk-in-Interview held on 25-02-2026 at NIELIT CoE Noida against advertisement No. NIELIT/NOIDA/CoE/2026/04.

The list of selected candidates (on contract basis) are as under: -

**Post: VLSI Design Expert/Junior VLSI Engineer (Contract Basis)**

| <b>S. No.</b> | <b>Candidate's Name</b> | <b>Father's / Husband's Name</b> | <b>Remarks</b> |
|---------------|-------------------------|----------------------------------|----------------|
| 1             | Ms. Jyoti               | Sh. Suresh Kumar                 | Selected       |
| 2             | Sh. Prabhav Thapliyal   | Sh. Alok Thapliyal               | Selected       |