



राष्ट्रीय इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी संस्थान, गोरखपुर (रा.इ.सू.प्रौ.सं.)
NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY, GORAKHPUR (NIELIT)
(इलेक्ट्रॉनिकी एवं सूचना प्रौद्योगिकी मंत्रालय, भारत सरकार की एक स्वायत्त वैज्ञानिक संस्था)
An Autonomous Scientific Society under Ministry of Electronics and
Information Technology (MeitY), Govt. of India

CoE in Chip Design, NIELIT Noida

I.O.N.

Date: 15-01-2025

This is to inform all the staff (Permanent/Contractual/WBL) of CoE in Chip Design, NIELIT Noida that a Centre visit of **Kendriya Vidyalaya Babugarh Cantt Hapur** is scheduled on 18/01/2025 Under PM Shri scheme at NIELIT Noida from 09:00 AM to 5:30 PM in two batches of class 8th. Tentative batch size is 40 students hence total 80 students in two batches are expected to visit the Centre. Therefore all the following staff is requested to report to the Centre at 8:30 AM on 18/01/2025.

1. Sh. Pankaj Shukla (Scientist E/ OIC NIELIT Noida)
2. Sh. Prashant Pal (Scientist 'C')
3. Sh. Mrityunjai Gautam (Faculty)
4. Sh. Jitesh Kumar (Faculty)
5. Ms. Nikhita Gondane (WBL Intern)
6. Ms. Samiksha Bharti (WBL Intern)
7. Ms. Deepali Yadav (WBL Intern)
8. Sh. Mukul Kumar (WBL Intern)
9. Sh. Nishant Shakya (WBL Intern)
10. Ms. Shalini Varshney (WBL Intern)
11. Sh. Kaushlendra Singh Rajawat (WBL Intern)
12. Sh. Pappu (Sweeper)

Officer In-Charge
CoE in Chip Design
NIELIT Noida

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